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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Bodo von Düring

Attorney Docket No.: ELIOP002

Patent: 6,803,145 B1

Issued: October 12, 2004

Title: FLAT LITHIUM CELL

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first class mail on November 4, 2004 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed:

Aurelia M. Sanchez

**REQUEST FOR CERTIFICATE OF CORRECTION
OF OFFICE MISTAKE
(35 U.S.C. §254, 37 CFR §1.322)**

Commissioner for Patents
P.O. Box 1450
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Attn: Certificate of Correction

**Certificate
NOV 10 2004
of Correction**

Dear Sir:

Attached is Form PTO-1050 (Certificate of Correction) at least one copy of which is suitable for printing. The errors together with the exact page and line number where they occur, and shown correctly in the application filed, are as follows:

COVER PAGE:

1. Insert the following section:

--[30] **Foreign Application Priority Data**

June 14, 2000 [CH] Switzerland.....1192/00--

NOV 12 2004

Patentee hereby requests expedited issuance of the Certificate of Correction because the error lies with the Office and because the error is clearly disclosed in the records of the Office. As required for expedited issuance, enclosed is documentation that unequivocally supports the patentee's assertion without needing reference to the patent file wrapper.

It is noted that the above-identified errors were printing errors that apparently occurred during the printing process. Accordingly, it is believed that no fees are due in connection with the filing of this Request for Certificate of Correction. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. ELIOP002).

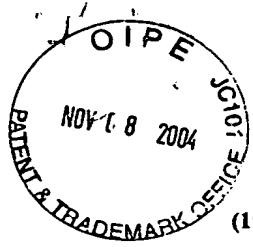
Respectfully submitted,
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NOV 12 2004



(12) NACH DEM VERTRAG ÜBER DIE INTERNATIONALE ZUSAMMENARBEIT AUF DEM GEBIET DES PATENTWESENS (PCT) VERÖFFENTLICHTE INTERNATIONALE ANMELDUNG

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Internationales Büro



(43) Internationales Veröffentlichungsdatum
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(10) Internationale Veröffentlichungsnummer
WO 01/97299 A1

(51) Internationale Patentklassifikation⁷: H01M 2/02, 10/04 (81) Bestimmungsstaaten (national): AE, AU, BR, CA, CN, CZ, HU, ID, IL, IN, JP, KR, KZ, LT, LV, MN, MX, NO, PL, RO, RU, SG, SI, US, UZ, VN, ZA.

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7. Juni 2001 (07.06.2001)

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(25) Einreichungssprache:

Deutsch

Veröffentlicht:

- mit internationalem Recherchenbericht
- vor Ablauf der für Änderungen der Ansprüche geltenden Frist; Veröffentlichung wird wiederholt, falls Änderungen eintreffen

(30) Angaben zur Priorität:

1192/00 14. Juni 2000 (14.06.2000) CH

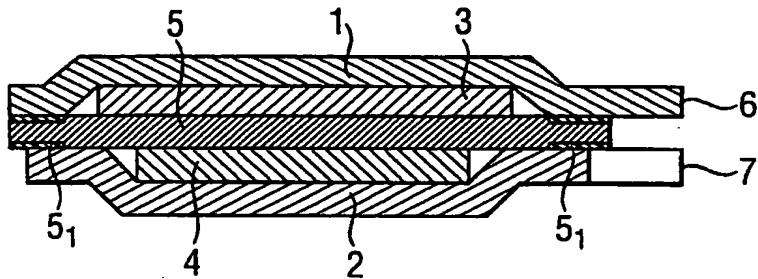
Zur Erklärung der Zweibuchstaben-Codes und der anderen Abkürzungen wird auf die Erklärungen ("Guidance Notes on Codes and Abbreviations") am Anfang jeder regulären Ausgabe der PCT-Gazette verwiesen.

(72) Erfinder; und

(75) Erfinder/Anmelder (nur für US): VON DÜRING, Bodo [CH/CH]; Furrengasse 5, CH-6004 Luzern (CH).

(54) Title: FLAT LITHIUM CELL

(54) Bezeichnung: LITHIUM-FLACHZELLE



A1

(57) Abstract: The invention relates to a flat lithium cell consisting of a first housing section (1) for accommodating a first thin-layer electrode (3), a second housing section (2) for accommodating a second thin-layer electrode (4) and a separator (5) located between the thin-layer electrodes. According to the invention, the housing sections are configured from electrically conductive films, the separator has an insulating sealing and adhesive material in the vicinity of the points of contact between the two housing sections and the two housing sections are connected to the separator by means of the sealing and adhesive material (5.1), which binds the sections to form a closed housing.

WO 01/97299

(57) Zusammenfassung: Lithium-Flachzelle, bestehend aus einem ersten Gehäuseteil (1) zur Aufnahme einer ersten Dünnenschicht-elektrode (3), einem zweiten Gehäuseteil (2) zur Aufnahme einer zweiten Dünnenschicht-elektrode (4) und einem zwischen den Dünn-schicht-elektroden angeordneten Separator (5), wobei die Gehäuseteile aus elektrisch leitenden Folien gebildet sind, der Separator im Bereich der Berührungsstellen der beiden Gehäuseteile isolierendes Siegel- und Haftmaterial aufweist und die beiden Gehäuseteile mittels des Siegels- und Haftmaterials (5.1) an dem Separator und durch diesen zu einem geschlossenen Gehäuse verbunden sind.

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(Also Form PT-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,803,145 B1

DATED : October 12, 2004

INVENTOR(S) : Bodo von Düring

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In the Cover Page:

Insert the following section:

--[30] **Foreign Application Priority Data**

June 14, 2000 [CH] Switzerland..... 1192/00--

MAILING ADDRESS OF SENDER:

PATENT NO. 6,803,145 B1

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